Harmonic Compensation and Reactive Power Support Using Ultracapacitor with Shunt Active Filter in Distribution system

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Abstract: Sustaining a quality of power supply is an important problems to the power Engineers. Owing to improvement in power quality a lot of power electronic devices and custom power devices are used in the industrial and commercial applications. Some of the non-linear loads and converters produces non-linear characteristics and create a flow of harmonic component in lines. Due to this effect, load voltages, load currents and the neutral current waveform due to unbalance gets distorted. In this project, the above effects are mitigated by neuro fuzzy Hysteresis Controller based ultracapacitor (UCAP) integration with an APF through a bi-directional dc-dc converter with in the phase conductor. By connecting filter in the line, load voltage becomes nearly sinusoidal and the distortion in the load and neutral current is reduced significantly. The MATLAB/Simulink model is used to verify this method.

Index Terms: Neuro Fuzzy Logic, Hysteresis Controller, Phase Locked Loop, Active Filter, ultra capacitor (UCAP), Coupling Transformer.

I. Introduction

Ultra capacitor are made up of double layered electromechanical capacitors, have established a lot of considerations in power conversion application. They are widely used in controlled industrial electric drives, traction and automotive drives, uninterruptible power supplies (UPSs), and active filters. It energy storage devices and their advantages over the conventional capacitors and electrochemical batteries are high energy and power density, high efficiency, high cycling capability, and long life.

The ac power system has viewed the problems concerning reactive power and unbalance . It has deteriorated with the increased use of power electronic converters as some of these converters not only increase reactive currents, but also generate harmonics in the source current. These power electronic converters are used in variable frequency AC motor drives, large power supplies, standby inverters and UPS, battery chargers, etc., which produces harmonics with large harmonic ranges. The current quality problem also concerned under unbalance. The increased reactive power, harmonics, and unbalance cause an increase in line losses, instability, and voltage distortion when harmonics increases and produce voltage drop across the line impedance, which spoils the power system. Conventionally, shunt passive filers are used to suppress the harmonics, and also provides reactive power compensation.

An UCAP bank into a unified power quality conditioner (UPQC) system through dc–dc converter is also providing active and reactive power support for the grid through the shunt APF is an another applications. The grids are connected with distributed energy sources through a three-phase grid-tied inverter which can be controlled to supplying active power, reactive power, harmonic compensation, and frequency regulation.

II. Three-Phase Grid Connected Inverter

A system diagram of the proposed system is shown in Fig. 1. The APF–UCAP is connected between the source and load with coupling transformer in shunt with the system. This system consists of a UCAP, bidirectional dc–dc converter which acts as an interface between the UCAP and the grid tied inverter. The advantage of this system is that ,it has the capability to supply and absorb active power from the grid .In UCAP there will be some voltage profile changes during charges/discharges it can be controlled by bi-directional dc–dc converter while the inverter dc-link voltage has to stay constant for accurate control of inverter. Due this converter act as a boost converter while the UCAP is discharging energy into the grid and during charging the UCAP from the grid it acts as buck converter .

A. Power Stage

The power stage that consists of a three-phase inverter and its controller is shown in Fig. 2. This system includes of an insulated gate bipolar transistor (IGBT) module, its gate driver, LC filter, and an isolation transformer. The dc-link capacitor voltage has to be regulated for optimum performance of the converter such that

 $V_{dc} = (2\sqrt{2n}/\sqrt{3m})Vab(rms)$ (1)

where m is the modulation index and n is the turns ratio of the isolation transformer. In this application for low-voltage distribution grids, the line–line rms voltage is 208 V, m is 0.75, and n is $\sqrt{3}$ for the delta–wye transformer which yields the required dc-link voltage Vdc of 260 V.



Fig. 1. Diagram of the APF–UCAP system.



Fig. 2. Proposed Model of three-phase shunt APF and its controller.

B. Controller Implementation

To control the three-phase shunt APF to provide harmonic and reactive power compensation of which the most common approaches are the p - q method [13], [14] and the i_d - i_q method [15]. In [15], the authors state that the i_d - i_q control performs better in nonsinusoidal and unbalanced conditions when compared to the p - q method, while both methods perform in a similar manner in balanced sinusoidal conditions. In this system, the id-iq method is modified to provide active and reactive power compensation instead of the reactive and harmonic compensation described in [15] such that i_d controls the reactive power and iq controls the active power [8]. Therefore, based on the references for active and reactive powers P the reference currents iqref and idref in d - q domain can be calculated using (2) and (3), where v is the system voltage in q-domain. Ideally, a higher-level integrated controller will be determining the values of Pref, Qrefsq based on various system inputs like Pgrid , Qgrid, Pload, Qload, Vdc, Vucap and Idclnk as shown in Fig. 2

 $P_{ref} = -3/2(v_{sq}i_{qref} + v_{sd}i_{dref}) \quad (2)$

If v_{sd} =0, then P_{ref} = -3/2vsqiqref. Similarly,

 $Q_{ref} = 3/2 (v_{sd} i_{qref} - v_{sq} i_{dref}) \qquad (3)$

and if $v_{sd} = 0$, then $Q_{ref} = -3/2(v_{sq}i_{dref})$

The reference currents in the d - q domain can be trans-formed into the reference currents i_{refa} , i_{refb} , and i_{refc} in the a - b - c domain using

$\begin{bmatrix} i_{\text{ref}a} \\ i_{\text{ref}b} \\ i_{\text{ref}c} \end{bmatrix} =$	$\begin{bmatrix} -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix}$	$\begin{bmatrix} \cos \theta \\ -\sin \theta \end{bmatrix}$	$\frac{\sin\theta}{\cos\theta} \left[\begin{array}{c} i_{dt} \\ i_{qt} \end{array} \right]$	$ ref _{ef} $]. (4)
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Equation (5) requires a phase-locked loop (PLL) to estimate the value of θ and the PLL used in this paper is based on the fictitious power method proposed in [14]. The main advantage of the fictitious power method is that it tracks the positive sequence voltage V+1 instead of the actual system voltage which is very useful in unbalanced systems. Once the reference currents i_{refa} , i_{refb} , and i_{refc} are calculated they are compared with the actual inverter currents i_{apf2a} , i_{apf2b} , and i_{apf2c} and the error is passed through a neuro fuzzy logic controller. There are various methods for current control like hysteresis, sigma–delta and PI control. Among these neuro fuzzy control is the most stable; however, it requires the tuning of controller gains based on the system parameters.

The output frequency is controlled by reference voltage to maintain regulation. The frequency counter is used to sense the supply frequence(Vabç). the supply frequency is compared with the reference frequency to obtain the error value. The compensated value is produced by the Neuro fuzzy controller and the compensated value is fed to the Phase Locked Loop (PLL). When the supply frequency is varied beyond the power quality limit, the frequency controlling system changes the required PLL from Neuro-fuzzy controller. The conventional control (PI), system is not suitable for non linear systems. ANN's is mostl used in the field of engineering as problem-solving tools. The fundamental element is a neuron which has multiple inputs and a single output. Each input is multiplied by a weight, the inputs are summed and this quantity is operated by the transfer function of the neuron to generate the output The output is sometimes referred to as an activity level.

C.Higher Level Integrated Controller

This controller is designed to make system level decisions on the inverter and dc–dc converter controller. The parameters like P_{load} , Q_{load} , P_{grid} , Q_{grid} , V_{ucap} , V_{dc} , I_{dclnk} , and I_{ucap} will decide on operating in four various modes. They are active power support mode, reactive power support mode, renewable intermittency smoothing mode, and UCAP charge mode.

a) Active power support mode

In both active power and renewable intermittency smoothing mode, the active power is supplied to the grid through UCAP-APF. Therefore, the active power capability of the UCAP-APF this can be evaluated by the higher level integrated controller. From the values of Pgrid and P_{load} , the reference P_{ref} is calculated in the higher level integrated controller and the P ref command is used to decided the UCAP storage and discharging capacity. If it has enough capacity, dc-dc converter controller is functioned in Grid Support Mode otherwise it is operated in Charge Mode where the UCAP is recharged to support the system whenever it requires power . In Grid Support Mode ,bi-directional converter operate in both Buck and Boost modes to respond to the active power requests and to regulate the dc-link voltage in a stable fashion while the inverter controller should respond such that the inverter through current control is used to produce command P_{ref} .

b) Reactive power support mode

In this mode the UCAP–APF system supplies reactive power to the grid and it does not provide any active power to the grid and even the APF losses are provided by the grid. From the values of Q_{grid} and Q_{load} , the reference value Q_{ref} is calculated in the integrated controller. In this mode, the dc–dc converter controller can be programmed to operate in Grid Support Mode directly since the active power requirement for operating in this mode is minimal. Therefore, the dc–dc converter controller is to regulate the dc-link voltage in a stable, while the commanded Qref is supplied by the inverter through current control.

c) Charge Mode

In this mode the UCAP is recharged by absorbing active power from the grid when the charge falls under 50%. The UCAP can be charged is controlled by the higher level integrated controller based on the P_{grid} and P_{load} values and the reference P_{ref} is calculated. The dc–dc converter will operate in Buck Mode to absorb the power from the grid then this operation is called charge mode and the inverter controller must respond to supply commanded P_{ref} .

III. Simulation Results

The simulation of the system which includes the three-phase grid tied inverter and the dc–dc converter is performed using MATLAB/SIMULINK. The commanded reactive power supply is simulated with i_{ref} = -15A which translates to a Q_{dref} of 3819Var from (3). The simulation results are shown in Fig. 3(a) and (b) where the UCAP is charged until 0.08 s, at which point the UCAP voltage E_{capv} reaches a value of 144 V. The UCAP is connected to the dc–dc converter at t =0.10 s.Att =0.12 s, the inverter is synchronized to the grid. It can be observed from Fig. 3(a) and (b) that P_{inv}, Q_{inv}, and V_{inv} have converged to the steadystate values of -92W, 3570 Var, and 260 V, respectively, closely tracking the commanded Q_{ref}. The inverter current and voltage waveforms when the system provides reactive power support are shown in Fig. 3(c) where it can be observed that I_{refa} tracks the reference current I_{apf2a} accurately after synchronization with the grid and Iapf2a lags V_{sa} by approximately 90 which is required for reactive power compensation. It can also be observed from voltage waveforms that the inverter voltage V_{inv1a} is almost sinusoidal after synchronization. The ability of the system to supply commanded active power is then simulated with i_{qref} =12.0A which translates to P_{ref} of 3054 W from (2). The simulation setup is the same as in the previous case and the simulation results are shown in Fig. 4(a) and (b) where it can be observed that Pinv, Q_{inv}, V_{fdc}, and I_{dclnkav} have converged to steady-state values of 2701W, 466Var, 260V,and 10.82A closely tracking the commanded P_{ref}.

Similarly, the simulation results for the renewable inter-mittency smoothing applications where the UCAP must be capable of both supplying and absorbing active power are shown in Fig. 5(a) and (b). Since the results for the case where the UCAP and the inverter system supply active power to the grid are already presented in Fig. 4(a) and (b), so in Fig. 5(a) and (b) similar results are presented for the case where the UCAP and inverter system absorb active power from the grid which is achieved by commanding a positive iqref of 7 A which corresponds to a Pinv of -1782 W. It can be observed from Fig. 5(a) and (b) that P_{ref} , and $I_{dclnkav}$ have converged to steady-state values of -1791 W, 260 V, and -6.906 A again tracking the commanded P closely. It should also be noted that simulation results are presented for shorter time span when compared to hardware results due to limitations in MATLAB/SIMULINK software. Therefore, it is evident from the simulations that the UCAP and APF system can together respond to changes in i_{qref} and i_{dref} commands and accordingly provide active and reactive power support to the grid. It is also evident that both the dc–dc converter and inverter can operate in a bi-directional fashion which is necessary when the system is used in renewable intermittency smoothing applications.

In this paper, the UCAP with APF design which provides active power support, reactive power support, and renewable intermittency smoothing to the low voltage distribution grid. The UCAP with the APF provides active power capability. The control strategy of the inverter which is a modified version of the $i_d - i_q$ method is ideal for supplying as well as absorbing the commanded active or reactive power to the grid. Major component like bi-directional dc–dc converter are also implemented to control UCAP and to maintain power flow . The dc–dc converter is controlled by average current mode control to regulate ouput voltage of converter due to its inherently stable characteristic. A higher level integrated controller which takes decisions based on the system parameters provides inputs to the inverter and dc–dc converter controllers to carry out their control actions





Fig. 3. (a) Currents and voltages of bi-directional dc-dc converter for $i_{dref} = -15.0$ A(reactive power support). (b) Grid, load, and inverter active and reactive power curves for $i_{dref} = -15.0$ A (reactive power support).(c) Currents and voltages of the inverter, grid, and load for idref = -15.0A(reactive power support).



Fig. 4. (a) Currents and voltages of bi-directional dc-dc converter for $i_{qref} = -12.0A$ (active power support). (b) Grid, load, and inverter active and reactive power curves for $i_{qref} = -12.0A$ (active power support; discharging active power).



Fig. 5. (a) Currents of bi-directional dc–dc converter for $i_{qref} = 7.0A$ (renewable intermittency smoothing; absorbing active power). (b) Grid, load, and inverter active power curves for $i_{qref} = 7.0A$ (renewable intermittency smoothing; absorbing active power).

IV. Conclusion

The simulation of the integrated system which consists of the UCAP, bi-directional dc-dc converter, and the grid-tied inverter is carried out using MATLAB/SIMULINK. Hardware experimental setup of the same integrated system is also possible to provide active power support, reactive power support and renewable intermittency smoothing to the distribution grid is dynamically tested through simulation.

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